Electronic Patent Application Fee Transmittal						
Application Number:	10	10708056				
Filing Date:	05-Feb-2004					
Title of Invention:	VE	METHOD AND SYSTEM TO VERIFY A CIRCUIT DESIGN BY VERIFYING CONSISTENCY BETWEEN TWO DIFFERENT LANGUAGE REPRESENTATIONS OF A CIRCUIT DESIGN				
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Filer:	Charles L. Moore/Katie Efland					
Attorney Docket Number:	361007.000031					
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Utility Filing Fees						
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
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Miscellaneous-Filing:						
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